



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IPW

The application of: **DeWitt, Jr. et al.**

Serial No.: **10/675,751**

Filed: **September 30, 2003**

For: **Method and Apparatus for
Debug Support for Individual
Instructions and Memory Locations**

35525

PATENT TRADEMARK OFFICE
CUSTOMER NUMBER

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Group Art Unit: **2183**

Examiner: **Cody, Dillon J.**

Attorney Docket No.: **AUS920030482US1**

Certificate of Mailing Under 37 C.F.R. § 1.8(a)

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By:

Louise Fay
Louise Fay

TRANSMITTAL DOCUMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

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- Supplemental Information Disclosure Statement;
- Form PTO-1449;
- Reference AB; and
- Our return postcard.

No fees are believed to be necessary. If, however, any fees are required, I authorize the Commissioner to charge these fees which may be required to Deposit Account No. 09-0447. No extension of time is believed to be necessary. If, however, an extension of time is required, the extension is requested, and I authorize the Commissioner to charge any fees for this extension to Deposit Account No. 09-0447.

Respectfully submitted,

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

DeWitt, Jr. et al.

Serial No: **10/675,751**

Filed: **September 30, 2003**

Title: **Method and Apparatus for Debug
Support for Individual Instructions and
Memory Locations**

Group Art Unit: **2183**

Intellectual Property
Law Department
International Business
Machines Corporation
11400 Burnet Road
Austin, Texas 78758

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the patentability of this application and in respect of which there may be a duty to disclose in accordance with 37 C.F.R. 1.56.

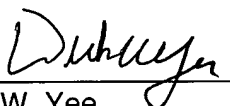
While this Supplemental Information Disclosure Statement may be "material" pursuant to 37 C.F.R. 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 C.F.R. 1.97(g) the filing of this Supplemental Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR 1.56 (b) exists.

No item of information cited in this Supplemental Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in this Supplemental Information Disclosure Statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of this Supplemental Information Disclosure Statement.

In the event a fee is required in connection with the enclosed Supplemental Information Disclosure Statement, the Commissioner of Patents and Trademarks is authorized to charge Deposit Account No. 09-0447 for the necessary amount.

Respectfully submitted,



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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

Complete if Known

Application Number	10/675,751
Filing Date	09/30/2003
First Named Inventor	DeWitt, Jr. et al.
Examiner Name	Cody, Dillon J.
Art Unit	2183
Attorney Docket No.	AUS920030482US1

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FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENT					
Examiner Initial	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	

NON PATENT LITERATURE

Examiner Initial	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	
	AB	Short, "Embedded Microprocessor Systems Design: An Introduction Using the Intel 80C188EB" Prentice-Hall, Inc.: 1998, p. 761.

Examiner:

Date Considered:

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.